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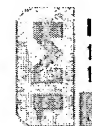
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4	6	((707/3 707/10).ccls.) and (controller same search\$3 same parallel)	USPAT	2004/07/14 16:18
6	2797	address adj1 cache	USPAT	2004/07/14 16:19
7	0	memory and edit and search and (controller same search\$3 same parallel) and (address adj1 cache)	USPAT	2004/07/14 16:20
8	3	(edit delete add) and search\$3 and (controller same search\$3 same parallel) and (address adj1 cache)	USPAT	2004/07/14 16:21
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12	13241	707/\$.ccls.	USPAT	2004/07/14 16:25
13	4190	search adj1 (engine module unit)	USPAT	2004/07/14 16:26
14	3	(central and priority) adj1 controller\$1	USPAT	2004/07/14 16:28
15	940	control\$6 and search\$3 and (parallel near3 execution)	USPAT	2004/07/14 16:29
16	59	(control\$6 and search\$3 and (parallel near3 execution)) and ((707/3 707/10).ccls.)	USPAT	2004/07/14 16:29
17	4165	search\$3 near5 level	USPAT	2004/07/14 16:30
18	0	((control\$6 and search\$3 and (parallel near3 execution)) and ((707/3 707/10).ccls.)) and (search\$3 near5 level) and (address adj1 cache)	USPAT	2004/07/14 16:30
19	1	(control\$6 and search\$3 and (parallel near3 execution)) and (search\$3 near5 level) and (address adj1 cache)	USPAT	2004/07/14 16:32
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20	14	FFS adj1 search	USPAT	2004/07/14 16:34
22	1	((control\$6 and search\$3 and (parallel near3 execution)) and ((707/3 707/10).ccls.)) and ((central priority) adj1 controller)	USPAT	2004/07/14 16:36
23	358	(search\$3 and controller).ab.	USPAT	2004/07/14 16:37
24	11	((search\$3 and controller).ab.) and ((707/3 707/10).ccls.)	USPAT	2004/07/14 16:43
25	233	priority adj1 controller	USPAT	2004/07/14 17:01
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29	6351	(simultaneous parallel) near9 (edit delete add modification)	USPAT	2004/07/14 17:04
30	1314990	level\$1	USPAT	2004/07/14 17:04
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32	2	((priority adj1 controller) (central adj1 controller)) and (binary adj1 search adj1 tree)	USPAT	2004/07/14 17:06
33	6	(IPv6 near5 protocol\$1) and ((707/3 707/10).ccls.)	USPAT	2004/07/14 17:10

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1 Adaptive resource management for flow-based IP/ATM hybrid switch systems

Hao Che; San-qi Li; Lin, A.;

Networking, IEEE/ACM Transactions on , Volume: 6 , Issue: 5 , Oct. 1998

Pages:544 - 557

[\[Abstract\]](#) [\[PDF Full-Text \(564 KB\)\]](#) **IEEE JNL**

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1 A method of tracking the peak power points for a variable speed wind energy conversion system

Datta, R.; Rangnanathan, V.T.;

Energy Conversion, IEEE Transactions on , Volume: 18 , Issue: 1 , March 2003
Pages:163 - 168

[\[Abstract\]](#) [\[PDF Full-Text \(533 KB\)\]](#) **IEEE JNL**

2 PIN-a PC-based robot simulation and offline programming system using macro programming techniques

Wenrui Dai; Kampker, M.;

Industrial Electronics Society, 1999. IECON '99 Proceedings. The 25th Annual Conference of the IEEE , Volume: 1 , 29 Nov.-3 Dec. 1999

Pages:442 - 446 vol.1

[Abstract] [PDF Full-Text (796 KB)] IEEE CNF

3 Probability based replacement algorithm for WWW server arrays

Yeung, K.H.; Suen, K.W.;

Parallel and Distributed Systems, 1998. Proceedings., 1998 International Conference on , 14-16 Dec. 1998

Pages:670 - 677

[\[Abstract\]](#) [\[PDF Full-Text \(120 KB\)\]](#) **IEEE CNF**

4 To exploit the collective intelligence thanks to the co-operative replication

Lancieri, L.; Bonnel, N.B.; Sturnme, L.;

Info-tech and Info-net, 2001. Proceedings. ICII 2001 - Beijing. 2001 International Conferences on , Volume: 4 , 29 Oct.-1 Nov. 2001

Pages:186 - 191 vol.4

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5 FPGA-based Internet Protocol Version 6 router

Mansour, M.; Kayssi, A.;

Computer Design: VLSI in Computers and Processors, 1998. ICCD '98.

Proceedings., International Conference on , 5-7 Oct. 1998

Pages:334 - 339

[\[Abstract\]](#) [\[PDF Full-Text \(64 KB\)\]](#) IEEE CNF

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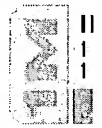
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1 A CMOS feedforward neural-network chip with on-chip parallel learning for oscillation cancellation*Liu, J.; Brooke, M.A.; Hirotsu, K.;*Neural Networks, IEEE Transactions on , Volume: 13 , Issue: 5 , Sep 2002
Pages:1178 - 1186[\[Abstract\]](#) [\[PDF Full-Text \(457 KB\)\]](#) IEEE JNL**2 Image processing LSI "ISP-IV" based on local parallel architecture and its applications***Muramatsu, S.; Kobayashi, Y.; Araoka, M.; Naoi, S.; Kaneta, T.; Hirose, K.; Onizawa, S.;*Image Processing, 1998. ICIP 98. Proceedings. 1998 International Conference on , 4-7 Oct. 1998
Pages:1000 - 1004 vol.3[\[Abstract\]](#) [\[PDF Full-Text \(651 KB\)\]](#) IEEE CNF

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1 Parallel search in matrices with sorted columns

Jain, A.;

Parallel and Distributed Processing, 1995. Proceedings. Seventh IEEE Symposium on , 25-28 Oct. 1995

Pages:224 - 230

[\[Abstract\]](#) [\[PDF Full-Text \(592 KB\)\]](#) **IEEE CNF**

2 An efficient parallel motion estimation algorithm for digital image processing

Liang-Gee Chen; Wai-Ting Chen; Yen-Shen Jehng; Chin-Ta Chuch;

Circuits and Systems, 1991., IEEE International Symposium on , 11-14 June
 Pages:670 - 673 vol.1

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3 Distributed game-tree search using transposition table driven work scheduling

Kishimoto, A.; Schaeffer, J.;

Parallel Processing, 2002. Proceedings. International Conference on , 18-21 Aug 2002

Pages:323 - 330

[\[Abstract\]](#) [\[PDF Full-Text \(298 KB\)\]](#) **IEEE CNF**

4 Load balancing strategies for parallel forward search algorithm with conflict based backjumping

Habbas, Z.; Herrmann, F.; Merel, P.-P.; Singer, D.;

Parallel and Distributed Systems, 1997. Proceedings., 1997 International Conference on , 10-13 Dec. 1997

Pages:376 - 381

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5 IDPS: a massively parallel heuristic search algorithm

Mahanti, A.; Daniels, C.J.;

Parallel Processing Symposium, 1992. Proceedings., Sixth International , 23-27 March 1992

Pages:220 - 223

[[Abstract](#)] [[PDF Full-Text \(304 KB\)](#)] IEEE CNF

6 A parallel tabu search and its hybridization with genetic algorithms

Matsumura, T.; Nakamura, M.; Tamaki, S.; Onaga, K.;

Parallel Architectures, Algorithms and Networks, 2000. I-SPAN 2000. Proceedings. International Symposium on , 7-9 Dec. 2000

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[[Abstract](#)] [[PDF Full-Text \(496 KB\)](#)] IEEE CNF

7 Optimal and near-optimal allocation of precedence-constrained task to parallel processors: defying the high complexity using effective search techniques

Ahmad, I.; Yu-Kwong Kwok;

Parallel Processing, 1998. Proceedings. 1998 International Conference on , 10-12 Aug. 1998

Pages:424 - 431

[[Abstract](#)] [[PDF Full-Text \(152 KB\)](#)] IEEE CNF

8 Parallel bidirectional heuristic search with dynamic process re-direction

Toptsis, A.A.;

Parallel Processing Symposium, 1994. Proceedings., Eighth International , 26-28 April 1994

Pages:242 - 247

[[Abstract](#)] [[PDF Full-Text \(508 KB\)](#)] IEEE CNF

9 Algorithms for search trees on message passing architectures

Colbrook, A.; Brewer, E.A.; Dellarocas, C.N.; Wehl, W.E.;

Parallel and Distributed Systems, IEEE Transactions on , Volume: 7 , Issue: 2 , Feb. 1996

Pages:97 - 108

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10 The APHID parallel $\alpha\beta$ search algorithm

Brockington, M.G.; Schaeffer, J.;

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Pages:428 - 432

[[Abstract](#)] [[PDF Full-Text \(512 KB\)](#)] IEEE CNF

11 **H-PBS: a hash-based scalable technique for parallel bidirectional s**
Nassimi, D.; Joshi, M.; Sohn, A.;
 Parallel and Distributed Processing, 1995. Proceedings. Seventh IEEE Sympos
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 Pages:414 - 421

[\[Abstract\]](#) [\[PDF Full-Text \(576 KB\)\]](#) IEEE CNF

12 **A parallel algorithm for the single step searching problem**
Lin, J.S.; Hsu, F.R.; Lee, R.C.T.;
 Parallel Architectures, Algorithms and Networks, 1994. (ISPAN) International
 Symposium on , 14-16 Dec. 1994
 Pages:278 - 285

[\[Abstract\]](#) [\[PDF Full-Text \(248 KB\)\]](#) IEEE CNF

13 **Parallel hybrid method for SAT that couples genetic algorithms and
 local search**
Folino, G.; Pizzuti, C.; Spezzano, G.;
 Evolutionary Computation, IEEE Transactions on , Volume: 5 , Issue: 4 , Aug.
 Pages:323 - 334

[\[Abstract\]](#) [\[PDF Full-Text \(248 KB\)\]](#) IEEE JNL

14 **Unsynchronized iteratively deepening parallel alpha-beta search**
Newborn, M.;
 Pattern Analysis and Machine Intelligence, IEEE Transactions on , Volume:
 10 , Issue: 5 , Sept. 1988
 Pages:687 - 694

[\[Abstract\]](#) [\[PDF Full-Text \(852 KB\)\]](#) IEEE JNL

15 **Parallel execution of an irregular algorithm depth first search (DFS
 heterogeneous clusters of workstation**
Ibrahim, M.A.M.; Xinda, L.; Rwakarambi, J.M.;
 Info-tech and Info-net, 2001. Proceedings. ICII 2001 - Beijing. 2001 Internat
 Conferences on , Volume: 3 , 29 Oct.-1 Nov. 2001
 Pages:328 - 332 vol.3

[\[Abstract\]](#) [\[PDF Full-Text \(390 KB\)\]](#) IEEE CNF

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